



जम्मू केंद्रीय विश्वविद्यालय
Central University of Jammu
राया-सूचानी (बागला), जिला सांबा-181143, जम्मू (जम्मू एवं कश्मीर)
Rahya-Suchani (Bagla), District: Samba – 181143, Jammu (J&K)

No: CUJ/Estab.T./ET-30/2026/ 1005

27.08.2026

अधिसूचना /Notification No. 66 /2026

Sub: Interview/ written test schedule for the posts advertised under Employment Notification No. 30 dated 17.03.2026 and Employment Notification No. 29 dated 25.02.2026 - Reg.

Ref: i) Employment Notification (EN) No. 30 dated 17.03.2026

ii) Employment Notification (EN) No. 29 dated 25.02.2026

It is hereby notified for the information of candidates that interviews for the following posts advertised vide notifications under reference will be held as per interview schedule given below:

INTERVIEW SCHEDULE				
S.No.	Name of the Department	Date of Interview	Post	Reporting Time
1.	Computer Science & Engineering	16.09.2026	Associate Professor	9:30 am
			Assistant Professor*	
2.	Electronics & Communication Engineering	17.09.2026	Associate Professor	9:30 am
			Assistant Professor*	

* As per the norms, the University will conduct written test for shortlisted candidates if the number of eligible candidates exceeds 20. Marks obtained by the candidates in the written test will be used exclusively for short listing them for the interview. The final selection will be based solely on the performance of the shortlisted candidates in the interview. Written Test will be of **objective type/MCQs** and minimum qualifying marks will be 50%. There will be **negative marking of 0.33 marks** for each incorrect answer.

Schedule of Written test is as follows:

WRITTEN TEST SCHEDULE				
Sl. No.	Name of the Department / Centre	Date of written test	Post	Reporting Time
1.	Computer Science & Engineering	12.09.2026	Assistant Professor	09:30 am
2.	Electronics & Communication Engineering	12.09.2026	Assistant Professor	12:30 pm

Syllabus for written test is attached herewith as Annexure A & B.

Important Note:

1. Final Scrutiny lists have been uploaded on University website.
2. Candidates who qualify written test for the post of Assistant Professor (Computer Science & Engineering and Electronics & Communication Engineering) will only be invited for the interview.
3. Eligible candidates (bearing Application form No.) for the post of Associate Professor will only be invited for interview.
4. Candidate can download Interview call letters from SAMARTH portal after uploading of final scrutiny lists and intimation through email this regard will also be sent to the candidates. The candidates are advised to regularly visit our website (www.cujammu.ac.in) for further updated information, if any.
5. Due to unavoidable circumstances, date of interviews may be changed. Candidates may regularly check email, University website and SAMARTH portal for further updation, if any.
6. Candidate who do not receive call letters, may request Establishment (Teaching) section through email teaching.recruitment@cujammu.ac.in at least two days prior to his/her interview for issue of duplicate /photo copy of call letter.
7. Candidates are required to report at least one hour prior to the time of interview / written test.



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8. The candidates are required to bring interview **call letter in original** along with **copy of application forms with self attested documents** (for submission) along with the following **original documents** for verification.
- Degree/Mark sheets clearly indicating percentage of marks.
 - Candidate belonging to SC/ST/OBC/PwBD/EWS categories are required to submit the recent certificate as per Govt. of India latest format at the time of interview.
 - Educational Qualifications, Experience Certificates, Category Certificate (if applicable), Original copy of papers published/ conferences attended/conference papers published/ books, awards and all other documents submitted online for the post etc.
 - Any other document relevant /required for the post as per the Employment Notification No. 30 dated 17.03.2026 and Employment Notification No. 29 dated 25.02.2026.
 - In service candidates are required to submit **No Objection Certificate (NOC)** and Vigilance Clearance certificate **without fail** at the time of Interview.
 - Recent certificate to be endorsed by the Head of the Institution to certify the No Vigilance Enquiry, Disciplinary action etc is pending /envisaged against the candidate.
 - The Candidates belonging to SC/ST/PwBD category only will be paid Railway/Bus fare by shortest route on production of tickets, as per Government of India rules.
 - Candidates are required to bring all original documents which have been indicated in the final screening list uploaded on University website without fail.**

Sd/-

कुलसचिव/Registrar

ईमेल:- registrar@cuammu.ac.in

दूरभाष. 91-8082197957

Section 1: Engineering Mathematics

Discrete Mathematics: Propositional and first order logic. Sets, relations, functions, partial orders and lattices. Monoids, Groups. Graphs: connectivity, matching, colouring. Combinatorics: counting, recurrence relations, generating functions.

Linear Algebra: Matrices, determinants, system of linear equations, eigenvalues and eigenvectors, LU decomposition.

Calculus: Limits, continuity and differentiability, Maxima and minima, Mean value theorem, Integration.

Probability and Statistics: Random variables, Uniform, normal, exponential, Poisson and binomial distributions. Mean, median, mode and standard deviation. Conditional probability and Bayes theorem.

Section 2: Digital Logic

Boolean algebra. Combinational and sequential circuits. Minimization. Number representations and computer arithmetic (fixed and floating point).

Section 3: Computer Organization and Architecture

Machine instructions and addressing modes. ALU, datapath and control unit. Instruction pipelining, pipeline hazards. Memory hierarchy: cache, main memory and secondary storage; I/O interface (interrupt and DMA mode).

Section 4: Programming and Data Structures

Programming in C. Recursion. Arrays, stacks, queues, linked lists, trees, binary search trees, binary heaps, graphs.

Section 5: Algorithms

Searching, sorting, hashing. Asymptotic worst case time and space complexity. Algorithm design techniques: greedy, dynamic programming and divide and conquer. Graph traversals, minimum spanning trees, shortest paths.

Section 6: Theory of Computation

Regular expressions and finite automata. Context-free grammars and push-down automata. Regular and context-free languages, pumping lemma. Turing machines and undecidability.

Section 7: Compiler Design

Lexical analysis, parsing, syntax-directed translation. Runtime environments. Intermediate code generation. Local optimisation, Data flow analyses: constant propagation, liveness analysis, common sub expression elimination.

Section 8: Operating System

System calls, processes, threads, interprocess communication, concurrency and synchronization. Deadlock. CPU and I/O scheduling. Memory management and virtual memory. File systems.

Section 9: Databases

ER model. Relational model: relational algebra, tuple calculus, SQL. Integrity constraints, normal forms. File organization, indexing (e.g., B and B+ trees). Transactions and concurrency control.

Section 10: Computer Networks

Concept of layering: OSI and TCP/IP Protocol Stacks; Basics of packet, circuit and virtual circuit-switching; Data link layer: framing, error detection, Medium Access Control, Ethernet bridging; Routing protocols: shortest path, flooding, distance vector and link state routing; Fragmentation and IP addressing, IPv4, CIDR notation, Basics of IP support protocols (ARP, DHCP, ICMP), Network Address Translation (NAT); Transport layer: flow control and congestion control, UDP, TCP, sockets; Application layer protocols: DNS, SMTP, HTTP, FTP, Email.

Section 1: Engineering Mathematics

Linear Algebra: Vector space, basis, linear dependence and independence, matrix algebra, eigen values and eigen vectors, rank, solution of linear equations - existence and uniqueness.

Calculus: Mean value theorems, theorems of integral calculus, evaluation of definite and improper integrals, partial derivatives, maxima and minima, multiple integrals, line, surface and volume integrals, Taylor series.

Differential Equations: First order equations (linear and nonlinear), higher order linear differential equations, Cauchy's and Euler's equations, methods of solution using variation of parameters, complementary function and particular integral, partial differential equations, variable separable method, initial and boundary value problems.

Vector Analysis: Vectors in plane and space, vector operations, gradient, divergence and curl, Gauss's, Green's and Stokes' theorems.

Complex Analysis: Analytic functions, Cauchy's integral theorem, Cauchy's integral formula, sequences, series, convergence tests, Taylor and Laurent series, residue theorem.

Probability and Statistics: Mean, median, mode, standard deviation, combinatorial probability, probability distributions, binomial distribution, Poisson distribution, exponential distribution, normal distribution, joint and conditional probability.

Section 2: Networks, Signals and Systems

Circuit Analysis: Node and mesh analysis, superposition, Thevenin's theorem, Norton's theorem, reciprocity. Sinusoidal steady state analysis: phasors, complex power, maximum power transfer. Time and frequency domain analysis of linear circuits: RL, RC and RLC circuits, solution of network equations using Laplace transform.

Linear 2-port network parameters, wye-delta transformation.

Continuous-time Signals: Fourier series and Fourier transform, sampling theorem and applications.

Discrete-time Signals: DTFT, DFT, z-transform, discrete-time processing of continuous-time signals. LTI systems: definition and properties, causality, stability, impulse response, convolution, poles and zeroes, frequency response, group delay, phase delay.

Section 3: Electronic Devices

Energy bands in intrinsic and extrinsic semiconductors, equilibrium carrier concentration, direct and indirect band-gap semiconductors.

Carrier Transport: Diffusion current, drift current, mobility and resistivity, generation and recombination of carriers, Poisson and continuity equations.

P-N junction, Zener diode, BJT, MOS capacitor, MOSFET, LED, photo diode and solar cell.

Section 4: Analog Circuits

Diode Circuits: Clipping, clamping and rectifiers.

BJT and MOSFET Amplifiers: Biasing, AC coupling, small signal analysis, frequency response. Current mirrors and differential amplifiers.

Op-amp Circuits: Amplifiers, summers, differentiators, integrators, active filters, Schmitt triggers and oscillators.

Section 5: Digital Circuits

Number Representations: Binary, integer and floating-point- numbers. Combinatorial circuits: Boolean algebra, minimization of functions using Boolean identities and Karnaugh map, logic gates and their static CMOS implementations, arithmetic circuits, code converters, multiplexers, decoders.

Sequential Circuits: Latches and flip-flops, counters, shift-registers, finite state machines, propagation delay, setup and hold time, critical path delay.

Data Converters: Sample and hold circuits, ADCs and DACs.

Semiconductor Memories: ROM, SRAM, DRAM.

Computer Organization: Machine instructions and addressing modes, ALU, data-path and control unit, instruction pipelining.

Section 6: Control Systems

Basic control system components; Feedback principle; Transfer function; Block diagram representation; Signal flow graph; Transient and steady-state analysis of LTI systems; Frequency response; Routh-Hurwitz and Nyquist stability criteria; Bode and root-locus plots; Lag, lead and lag-lead compensation; State variable model and solution of state equation of LTI systems.

Section 7: Communications

Random Processes: Auto correlation and power spectral density, properties of white noise, filtering of random signals through LTI systems.

Analog Communications: Amplitude modulation and demodulation, angle modulation and demodulation, spectra of AM and FM, super heterodyne receivers.

Information Theory: Entropy, mutual information and channel capacity theorem.

Digital Communications: PCM, DPCM, digital modulation schemes (ASK, PSK, FSK, QAM), bandwidth, inter-symbol interference, MAP, ML detection, matched filter receiver, SNR and BER. Fundamentals of error correction, Hamming codes, CRC.

Section 8: Electromagnetics

Maxwell's Equations: Differential and integral forms and their interpretation, boundary conditions, wave equation, Poynting vector.

Plane Waves and Properties: Reflection and refraction, polarization, phase and group velocity, propagation through various media, skin depth.

Transmission Lines: Equations, characteristic impedance, impedance matching, impedance transformation, S-parameters, Smith chart. Rectangular and circular waveguides, light propagation in optical fibers, dipole and monopole antennas, linear antenna arrays